

## Electronic Acknowledgement Receipt

<b>EFS ID:</b>	4062045
<b>Application Number:</b>	10518974
<b>International Application Number:</b>	
<b>Confirmation Number:</b>	2918
<b>Title of Invention:</b>	Microprocessor instruction execution method for exploiting parallelism by time ordering operations in a single thread at compile time
<b>First Named Inventor/Applicant Name:</b>	Richard Michael Taylor
<b>Customer Number:</b>	20802
<b>Filer:</b>	Theodore R. West
<b>Filer Authorized By:</b>	
<b>Attorney Docket Number:</b>	5035-202US//P29,653
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### Payment information:

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### File Listing:

Document Number	Document Description	File Name	File Size(Bytes)/ Message Digest	Multi Part /.zip	Pages (if appl.)
1		int2A.PDF	76466	yes	2
			684f9f3e496887f474998db9f4143a08fc87ae28		

	Multipart Description/PDF files in .zip description		
	Document Description	Start	End
	Assignee showing of ownership per 37 CFR 3.73(b).	1	1
	Power of Attorney	2	2
Warnings:			
Information:			
Total Files Size (in bytes):		76466	
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